

## CLAIMS

What is claimed is:

5                   1. A single time slot based data burst, comprising:  
                    a plurality of time slot based data frames;  
                    less than all of said plurality of time slot based data frames  
including a sync word, remaining ones of said plurality of time slot based  
data frames not including a sync word.

10                  2. The single time slot based data burst according to claim  
1, wherein:  
                    said time slot based data frames are TDMA data frames.

15                  3. The single time slot based data burst according to claim  
1, wherein:  
                    said sync word is included at a beginning of said less than  
all of said plurality of time slot based data frames.

20                  4. The single time slot based data burst according to claim  
1, wherein:  
                    only one of said plurality of time slot based data frames  
includes a sync word.

25                  5. The single time slot based data burst according to claim  
1, wherein:  
                    said remaining ones of said plurality of time slot based data  
frames include data payload in a position containing said sync word in  
said less than all of said plurality of time slot based data frames.

6. Apparatus for receiving a time slot based data burst,  
comprising:

a receiver to receive a time slot based burst containing a  
plurality of frames, less than all of said frames including a sync word;

5 a master clock;

a data clock transition position determiner to determine a  
position of an active edge of said master clock with respect to received  
data;

10 wherein said data clock transition position determiner  
adjusts a frequency of said master clock to maintain a centering of said  
active edge of said master clock within a respective portion of said  
received data.

15 7. The apparatus for receiving a time slot based data burst  
according to claim 6, wherein:

said time slot based burst is a TDMA burst.

8. A method of receiving time slot based burst data,  
comprising:

20 receiving a time slot based burst containing a plurality of  
frames;

decoding a sync word in less than all frames of said time slot  
based burst; and

25 controlling a centering of an active edge of a master clock  
with respect to at least one symbol in at least one of said plurality of  
frames.

9. The method of receiving time slot based burst data according to claim 8, wherein:

said at least one symbol is in a last one of said plurality of frames in said time slot based burst.

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10. The method of receiving time slot based burst data according to claim 8, wherein:

said at least one symbol is a last symbol in a last one of said plurality of frames in said time slot based burst.

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11. Apparatus for receiving time slot based burst data, comprising:

means for receiving a time slot based burst containing plurality of frames;

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means for decoding a sync word in less than all frames of said time slot based burst; and

means for controlling a centering of an active edge of a master clock with respect to at least one symbol in at least one of said plurality of frames.

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12. The apparatus for receiving time slot based burst data according to claim 11, wherein:

said at least one symbol is in a last one of said plurality of frames in said time slot based burst.

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13. The apparatus for receiving time slot based burst data according to claim 11, wherein:

said at least one symbol is a last symbol in a last one of said plurality of frames in said time slot based burst.

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